

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): KAJIHARA, et al.
Serial No.: 09/328,910
Filed: June 9, 1999
For: LEADFRAME SEMICONDUCTOR INTEGRATED CIRCUIT
DEVICE USING THE SAME AND METHOD OF AND
PROCESS FOR FABRICATING THE TWO
Group: 2815
Examiner: Jasmine Jhihan B Clark
Conf. No.: 7455

RESPONSE

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

July 5, 2011

Sir:

In response to the Office Action dated March 4, 2011, please amend the
above-identified application as listed below and as set forth on the following pages:

Listing of the Claims

Remarks are included following the amendments